

Fig. 1

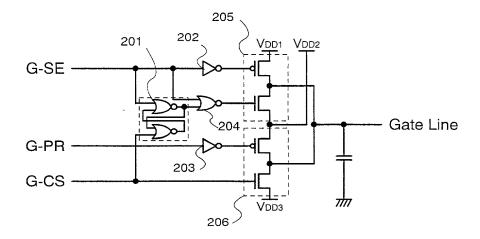
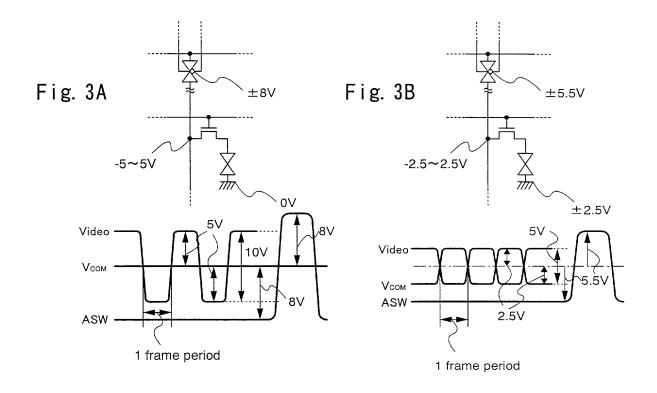
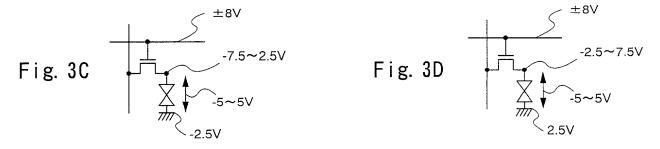
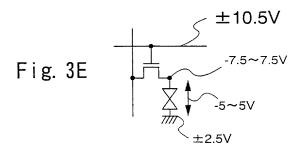
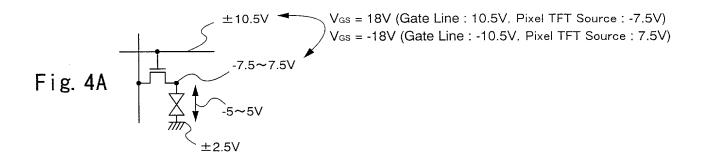


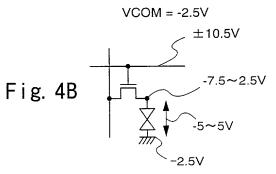
Fig. 2



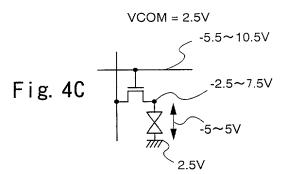








 $V_{GS} = 18V$  (Gate Line : 10.5V, Pixel TFT Source : -7.5V)  $V_{GS} = -13V$  (Gate Line : -10.5V, Pixel TFT Source: 2.5V)



 $V_{\text{GS}} = 13 \text{V (Gate Line : 10.5V, Pixel TFT Source : -2.5V)}$   $V_{\text{GS}} = -13 \text{V (Gate Line : -5.5V, Pixel TFT Source: 7.5V)}$ 

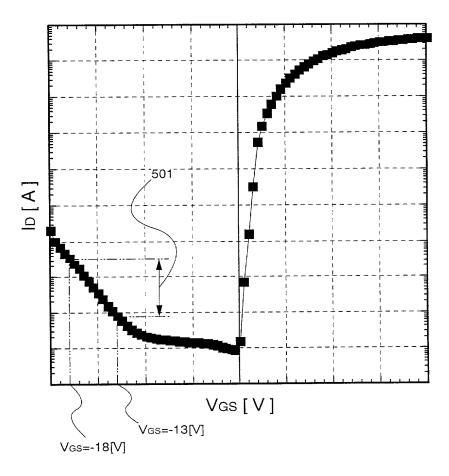
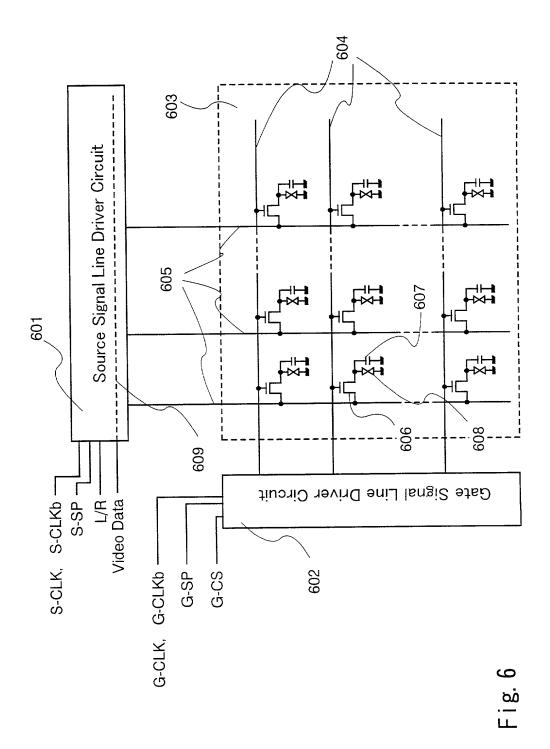


Fig. 5

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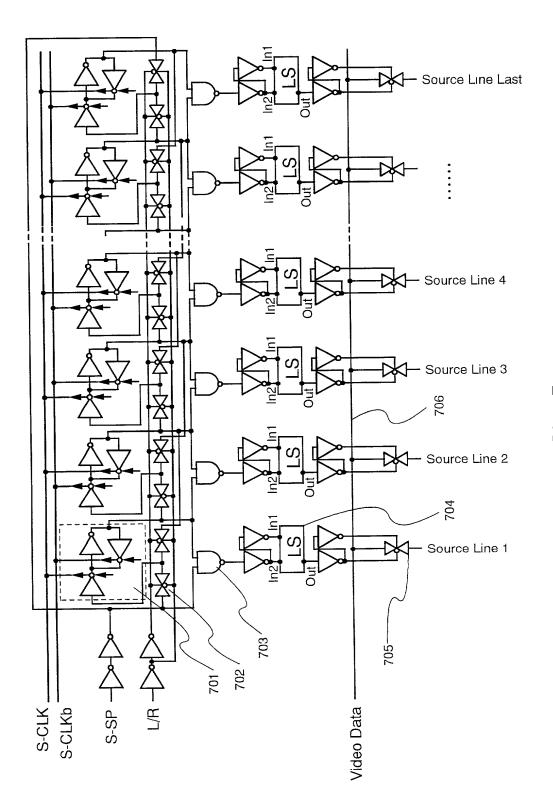


Fig. 7

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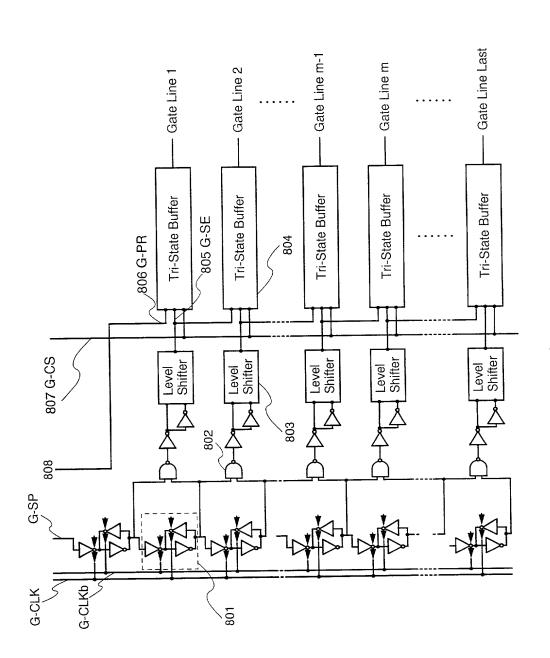
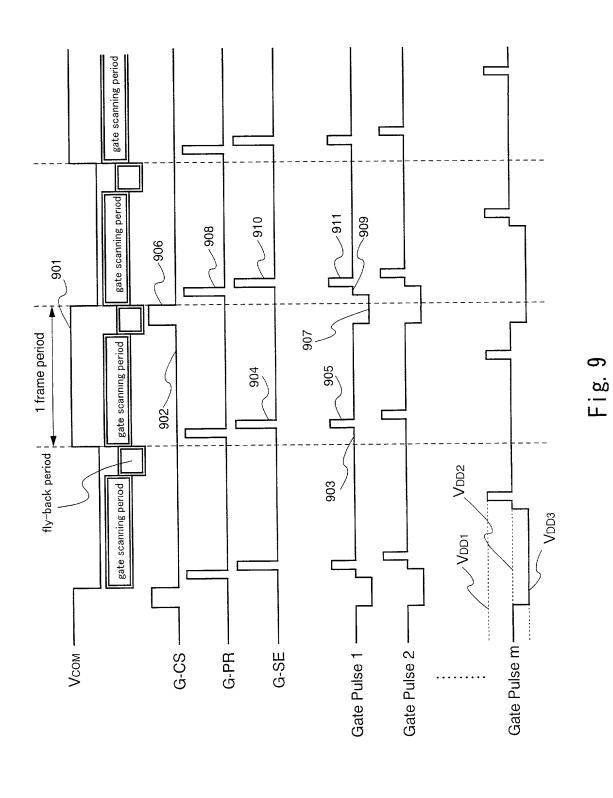


Fig. 8



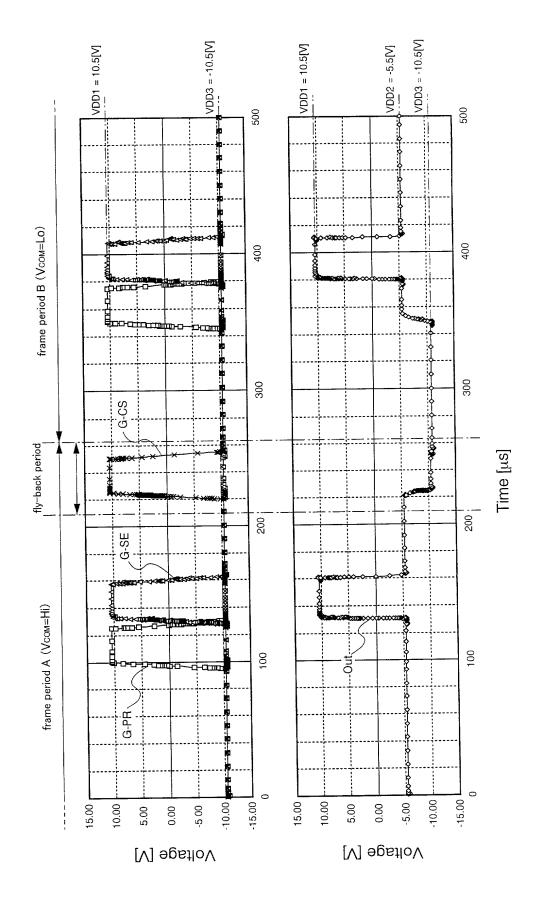


Fig. 10

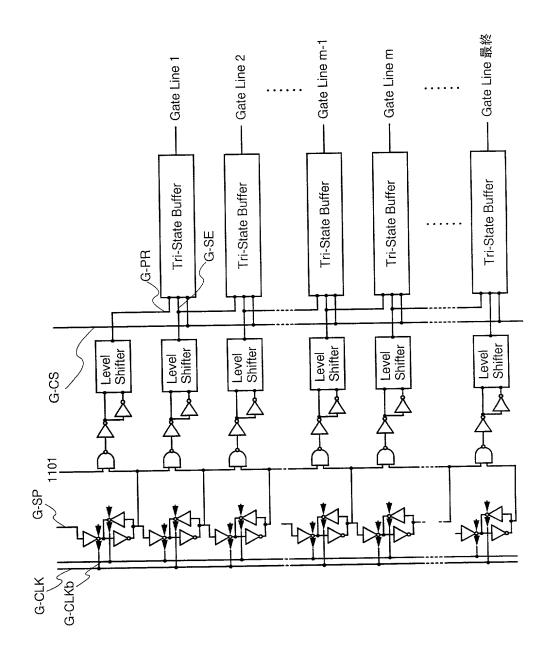
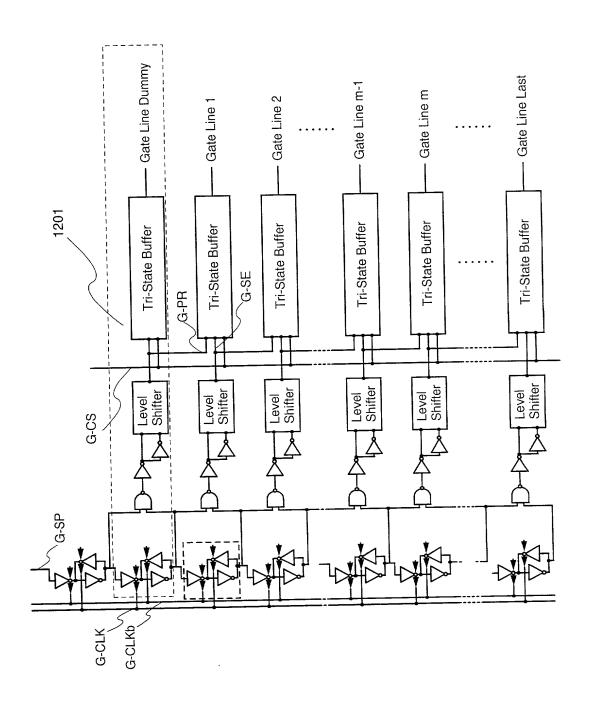


Fig. 1

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Fig. 12

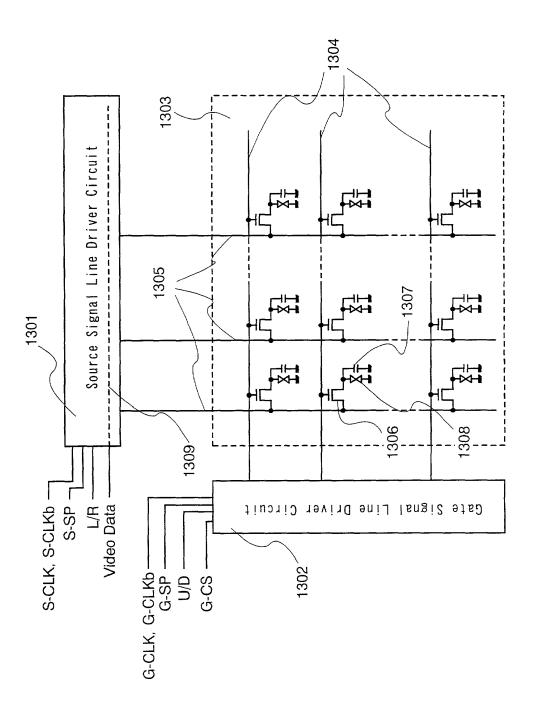
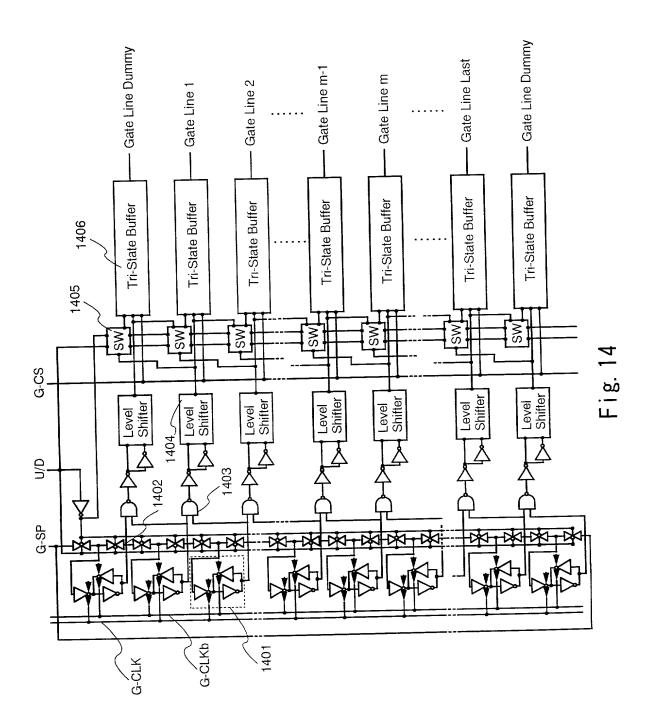
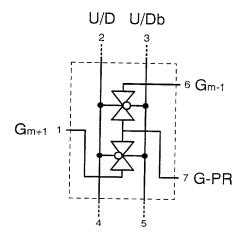


Fig. 13



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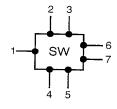
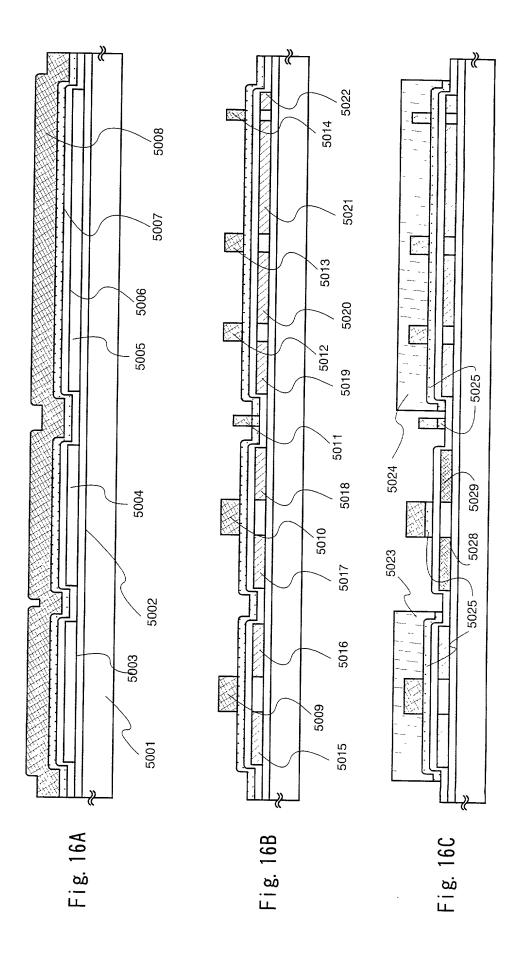
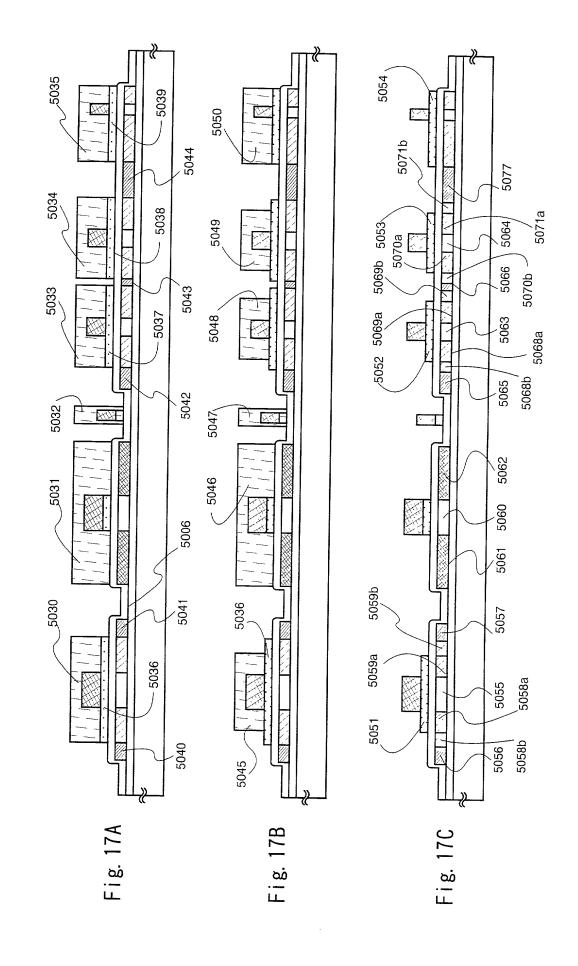


Fig. 15





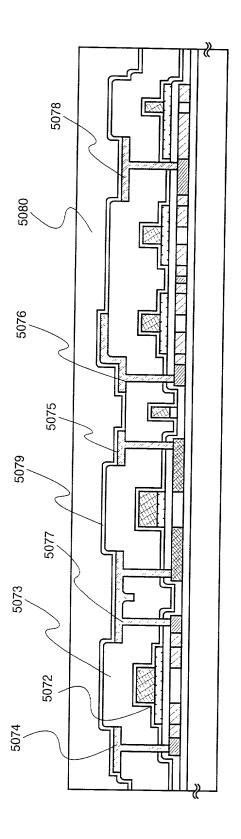


Fig. 18

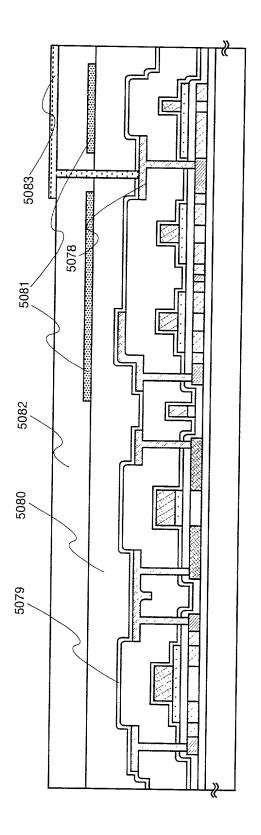


Fig. 19

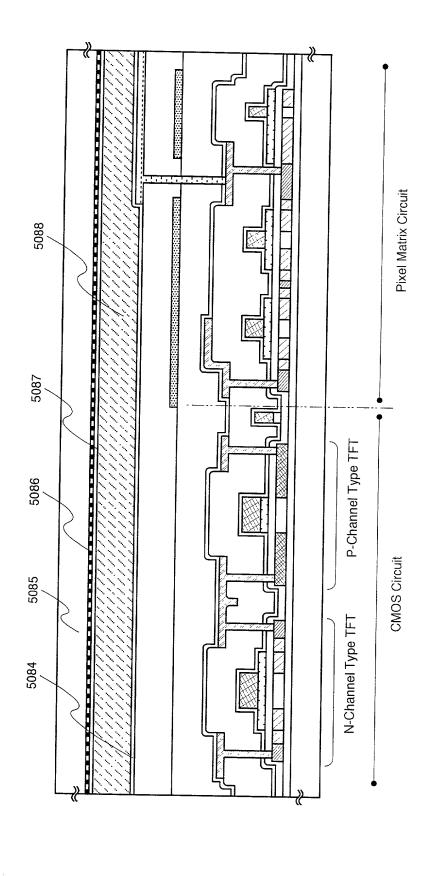


Fig. 20

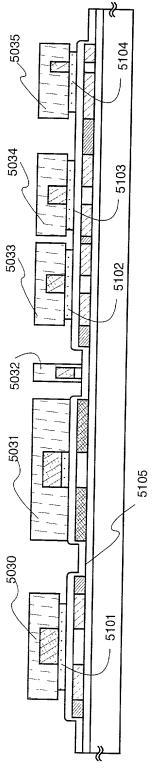
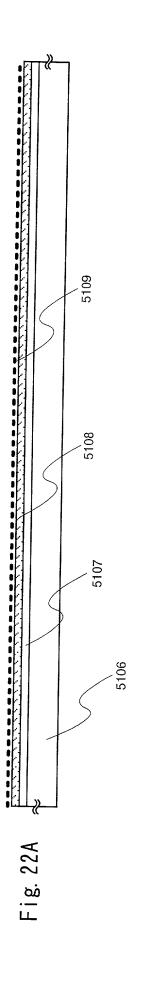
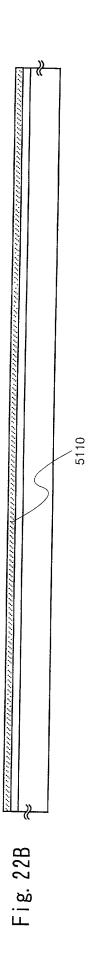
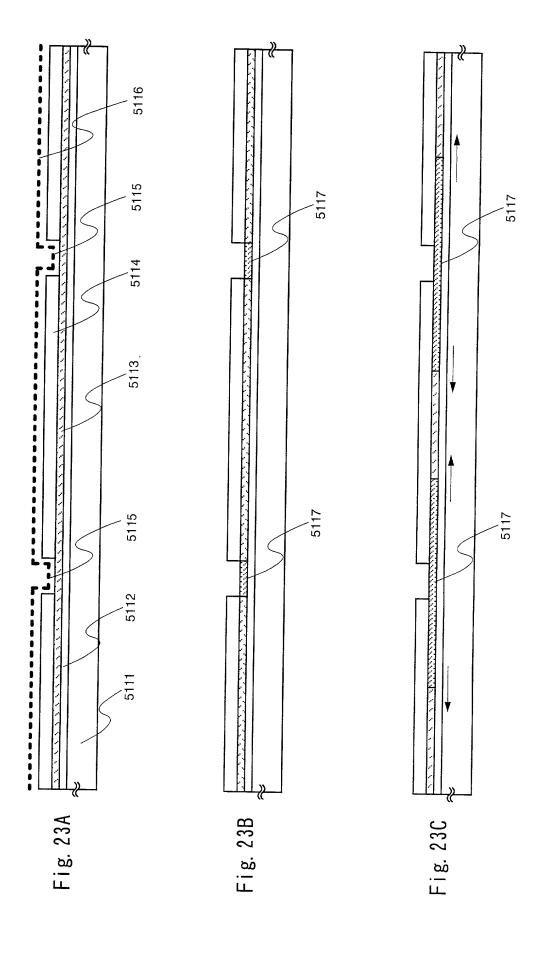


Fig. 21

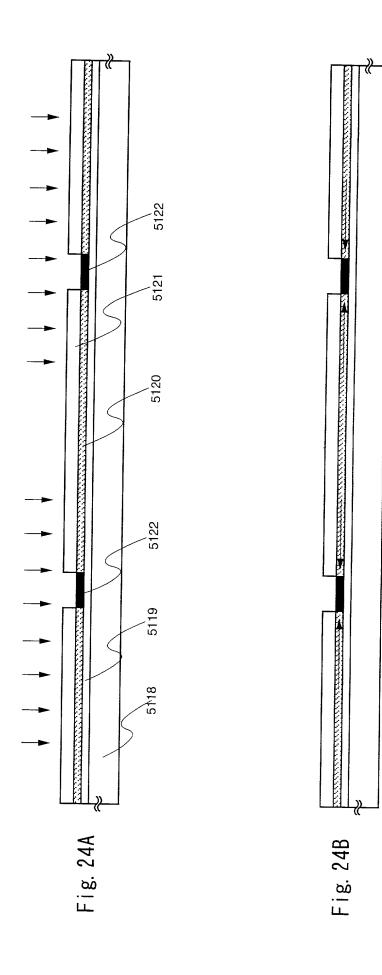
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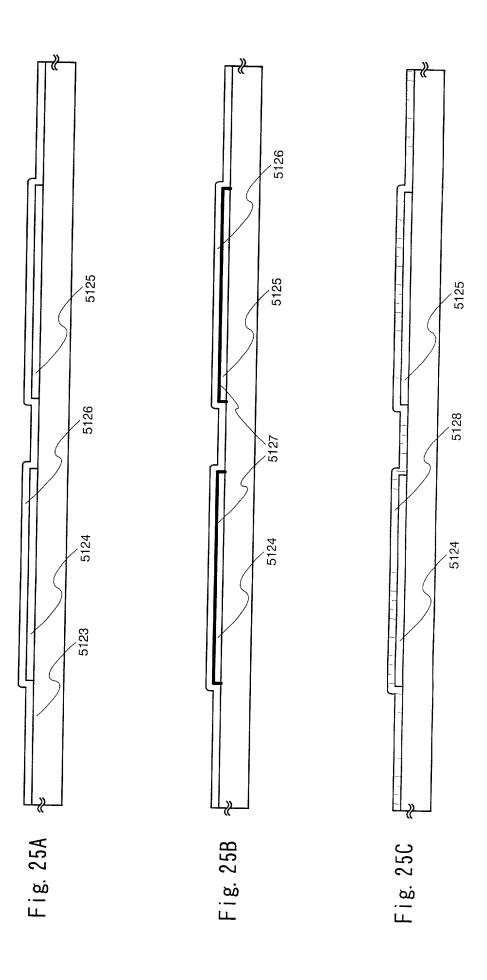


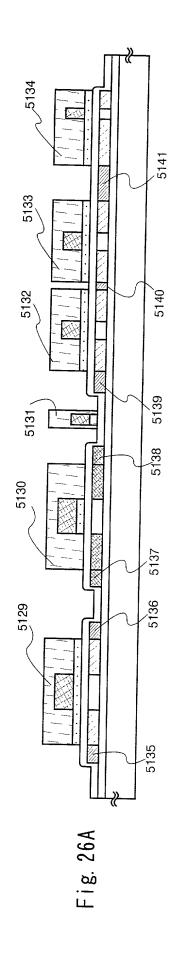


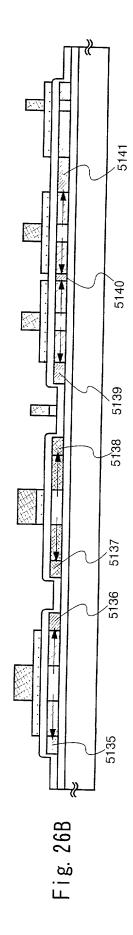
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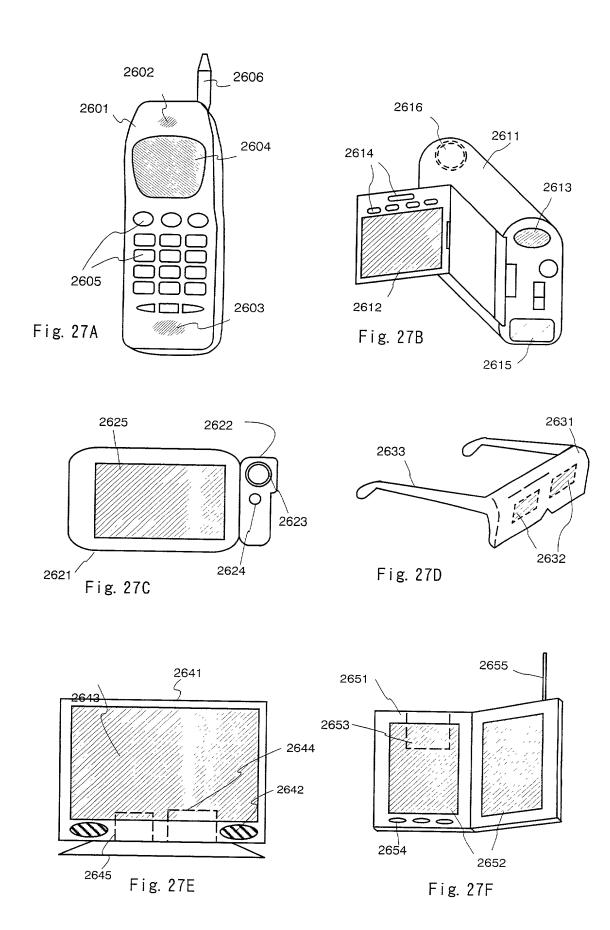


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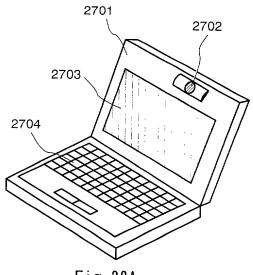


Fig. 28A

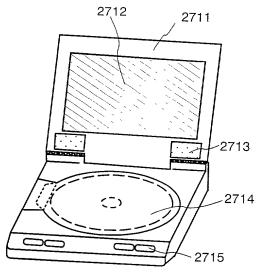


Fig. 28B

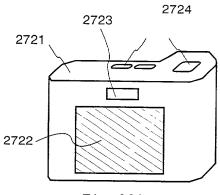


Fig. 28C

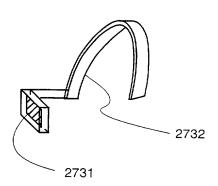


Fig. 28D

